

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NUM	FILING DATE	CLASS	SUBCLASS	GAU	2014	EXAMINER
10083533	02/27/2002	438	315	2812		Thao Le

**APPLICANTS: Hashimoto Hiroshi; Takahashi Koji;

CONTINUING DATA VERIFIED: No
TL

** FOREIGN APPLICATIONS VERIFIED: Yes
JAPAN 2001-205188 07/05/2001 TL

PG. PUB. DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO.
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		020244
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE: Semiconductor integrated circuit device and method of producing the same		

NOTICE OF ALLOWANCE BASED		CLARK ALL 750	
Assistant Examiner		DRAWING	
Amount Due		Sheets Drawg.	Print Fig.
Date Paid		Application Examiner	
TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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